# PROLABS - DS-SFP-FC8G-SW-C

8.5GBd SFP+ Short Wavelength (850nm) Transceiver

# DS-SFP-FC8G-SW-C Overview

PROLABS's DS-SFP-FC8G-SW-C SFP+ optical transceivers are based on Fiber Channel Links up to 8.5Gb/s data rate over multimode fiber, and they are compliant with PC-PI-4 Rev 7.0 and SFF-8472 Rev 10.1 and compatible with SFF-8432 and applicable portions of SFF-8431 Rev 1.3.

### **Product Features**

- Up to 8.5 GBd bi-directional data links
- Compliant with SFF 8431 & SFF 8432
- Hot-pluggable SFP+ footprint
- 850nm VCSEL laser transmitter
- Duplex LC connector
- Built-in digital diagnostic functions
- Up to 150m on OM3 MMF
- Single power supply 3.3V
- RoHS Compliance
- Class 1 laser product complies with EN 60825-1
- Operating temperature range: 0°C to 70°C.

#### **Applications**

- 2.125G Fiber Channel
- 4.25G Fiber Channel
- 8.5G Fiber Channel

**Ordering Information** 

Ordering information	
Part Number	Description
DS-SFP-FC8G-SW-C	8.5G SFP+ 850nm LC Connectors 150m on MMF, with DOM function.

**General Specifications** 

Parameter	Symbol	Min	Тур	Max	Unit		Remarks	
Data Rate	DR		8.5		GBd	IEEE	802.3ae	
Bit Error Rate	BER			$10^{-12}$				
Operating Temperature	$T_{OP}$	0		70	°C	Case	temperature	е
Storage Temperature	$T_{STO}$	<b>- 40</b>		85	°C	Ambi	ent tempera	ture
Supply Current	1		180	220	mΛ	For	electrical	power
Supply Current	IS		100	220	mA	interface		
Input Voltage	$V_{CC}$	3	3.3	3.6	V			
Maximum Voltage	$V_{MAX}$	0.5	•	4	V	For	electrical	power
waxiiiiuiii voltage	<b>v</b> MAX	<b>- 0.5</b>		4	V	interface		



Optical Characteristics – Transmitter  $V_{\rm CC}$ =3V to 3.6V,  $T_{\rm C}$ =0 ${\mathcal C}$  to 70 ${\mathcal C}$ 

Parameter	Symbol	Min	Тур	Max	Unit	Remarks
Output Optical Power	$P_{TX}$	<b>- 9</b>		<b>– 1</b>	dBm	Class 1 Product
Optical Center Wavelength	$\lambda_{C}$	830		860	nm	
Optical Modulation Amplitude @ 2.125G	OMA	196			uW	IEEE 802.3ae
Optical Modulation Amplitude @ 4.25G	OMA	247			uW	IEEE 802.3ae
Optical Modulation Amplitude @ 8.5G	OMA	302			uW	IEEE 802.3ae
Extinction Ratio	ER	3	5.5		dB	
Spectral Width (RMS)	$\Delta\lambda$			0.65	nm	
Relative Intensity Noise	RIN			- 128	dB/Hz	
Transmitter Dispersion Penalty	TDP			4.2	dB	
Transmitter Jitter		Acc	cording to	<b>IEEE 802</b>	.3ae requ	irement
Launch Power of OFF Transmitter	$P_{OUT\_OFF}$			- 30	dBm	Average

Optical Characteristics – Receiver

Parameter	Symbol	Min	Тур	Max	Unit	Remarks
Optical Center Wavelength	λ <sub>C</sub>	770		860	nm	
Receiver Sensitivity (OMA)@ 2.125G	R <sub>X_SEN1</sub>			<b>- 49</b>	uW	Measured with worst ER: BER<10 <sup>-12</sup> 2 <sup>31</sup> -1 PRBS
Receiver Sensitivity (OMA)@ 4.25G	$R_{X\_SEN2}$			- <b>61</b>	uW	Measured with worst ER: BER<10 <sup>-12</sup> 2 <sup>31</sup> -1 PRBS
Receiver Sensitivity (OMA)@ 8.5G	R <sub>X_SEN3</sub>			- <b>7</b> 6	uW	Measured with worst ER: BER<10 <sup>-12</sup> 2 <sup>31</sup> -1 PRBS
Maximum Input Power	P <sub>IN</sub>	0.5			dBm	
Optical Return Loss	ORL	12			dB	
LOS Assert	$LOS_A$	- 30			dBm	
LOS De-Assert	$LOS_D$			<b>–</b> 18	dBm	
LOS Hysteresis		0.5			dB	



# Electrical Characteristics – Transmitter $V_{CC}=3V$ to 3.6V, $T_{C}=0$ °C to 70°C

Parameter	Symbol	Min	Тур	Max	Unit	Remarks
Input differential impedance	$R_{IN}$		100		Ω	Non condensing
Single ended data input swing	$V_{IN\_PP}$	90		800	mV	
Transmit disable voltage	$V_D$	2		V <sub>cc</sub>	V	
Transmit enable voltage	$V_{EN}$	V <sub>EE</sub>		V <sub>EE</sub> +0.	V	
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# **Electrical Characteristics - Receiver**

 $V_{CC}=3V$  to 3.6V,  $T_{C}=0$ °C to 70°C

Parameter	Symbol	Min	Тур	Max	Unit	Remarks
Single ended data output swing	$V_{OUT\_PP}$	170	300	400	mV	
Data output rise/fall time (20%-80%) @2.125G & 4.25G	$T_R/T_F$			120	ps	
Data output rise/fall time (20%-80%) @2.125G & 4.25G	$T_R/T_F$			60	ps	
LOS Fault	V <sub>LOS_Fault</sub>	2		V <sub>CC_HO</sub>	V	
LOS Normal	$V_{LOS\_normal}$	$V_{EE}$		V <sub>EE</sub> +0.	V	

## **Digital Diagnostic Functions**

DS-SFP-FC8G-SW-C support the 2-wire serial communication protocol as defined in the SFF 8472. Digital diagnostic information are accessible over the 2-wire interface at the address 0xA2. Digital Diagnostics for DS-SFP-FC8G-SW-C are internally calibrated by default. A micro controller unit inside the transceiver gathers the monitoring information and reports the status of transceiver.

**Transceiver Temperature**, internally measured, represented as a 16 bit signed twos complement value in increments of 1/256 degrees Celsius, Temperature accuracy is better than ±3 degrees Celsius over specified operating temperature and voltage.

**Transceiver Supply Power,** internally measured, represented as a 16 bit unsigned integer with the voltage defined as the full 16 bit value (0-65535) with LSB equal to 100  $\mu$ Volt, yielding a total range of 0 to +6.55 Volts.

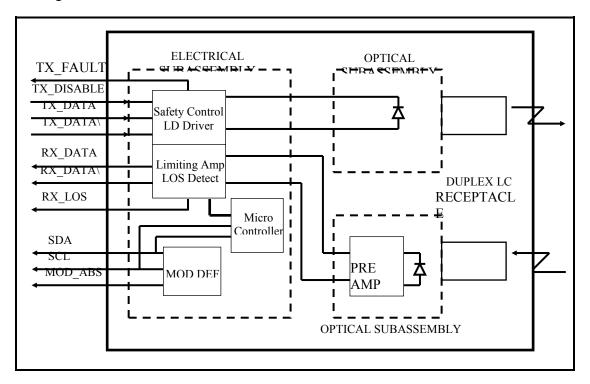
**Transceiver TX bias current,** internally measured, represented as a 16 bit unsigned integer with the current defined as the full 16 bit value (0 - 65535) with LSB equal to 2  $\mu$ A, yielding a total range of 0 to 131mA. Accuracy is better than  $\pm 10\%$  over specified operating temperature and voltage.

**Transceiver TX output power,** internally measured, represented as a 16 bit unsigned integer with the power defined as the full 16 bit value (0 - 65535) with LSB equal to 0.1  $\mu$ W. Data is assumed to be based on measurement of laser monitor photodiode current. Accuracy is better than  $\pm 3$ dB over specified temperature and voltage. Data is not valid when the transmitter is disabled.

**Transceiver RX received optical power,** internally measured, represented as a 16 bit unsigned integer with the power defined as the full 16 bit 35 value (0-65535) with LSB equal to 0.1  $\mu$ W. Accuracy is better than  $\pm 3$ dB over specified temperature and voltage.

Parameter	Symbol	Accuracy	Units	Repor	t Range	Unit	Remarks
Temperature	T <sub>MON</sub>	±3	°C	- 5	75	°C	
Voltage	$V_{MON}$	±0.1	V	2.9	3.7	V	
Bias Current	I <sub>MON</sub>	±10	%	1	15	mA	
Tx Power	P <sub>MON</sub>	±3	dB	- 10	0	dBm	
Rx Power	P <sub>MON</sub>	±3	dB	<b>- 20</b>	0	dBm	

#### **Block Diagram of Transceiver**



#### **Transmitter Section**

The VCSEL driver accept differential input data and provide bias and modulation currents for driving a laser. An automatic power-control (APC) feedback loop is incorporated to maintain a constant average optical power. 850 nm VCSEL in an eye safe optical subassembly (OSA) mates to the fiber cable.

### TX DISABLE

The TX\_DISABLE signal is high (TTL logic "1") to turn off the laser output. The laser will turn on within 1ms when TX\_DISABLE is low (TTL logic "0").

# TX\_FAULT

When the TX\_FAULT signal is high, output indicates a laser fault of some kind. Low indicates normal operation.

### **Receiver Section**

The receiver utilizes a PIN detector integrated with a trans-impedance preamplifier in an OSA. This OSA is connected to a Limiting Amplifier which providing post-amplification quantization, and optical signal detection. The limiting Amplifier is AC-coupled to the transimpedance amplifier, with internal  $100\Omega$  differential termination.

#### Receive Loss (RX LOS)

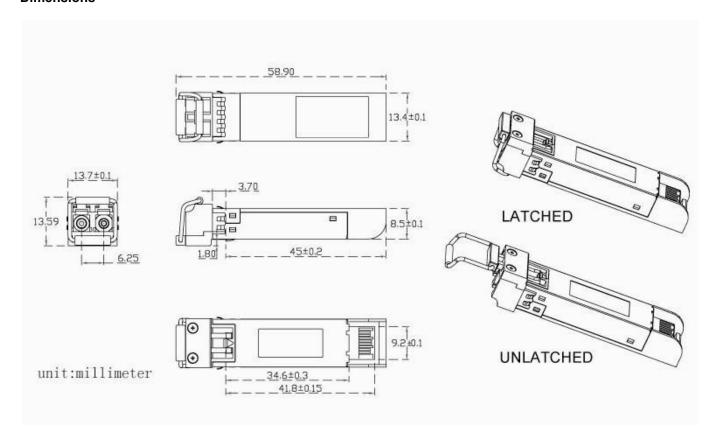
The RX\_LOS is high (logic "1") when there is no incoming light from the companion transceiver. This signal is normally used by the system for the diagnostic purpose. The signal is operated in TTL level.

## **Controller Section**

The micro controller unit monitors the operation information of LD driver and Limiting Amplifier. And report these status to the customer.



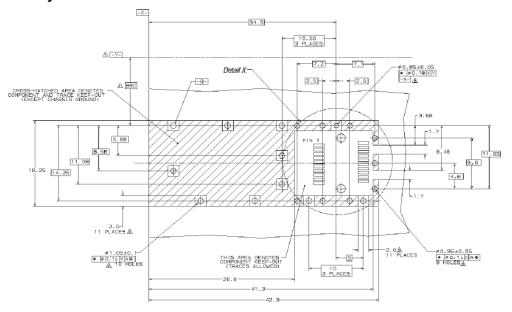
### **Dimensions**



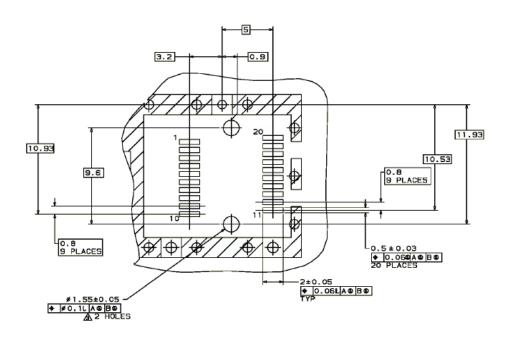
ALL DIMENSIONS ARE ±0.2mm UNLESS OTHERWISE SPECIFIED UNIT: mm



### **PCB Layout Recommendation**

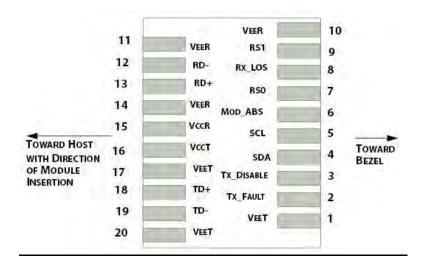


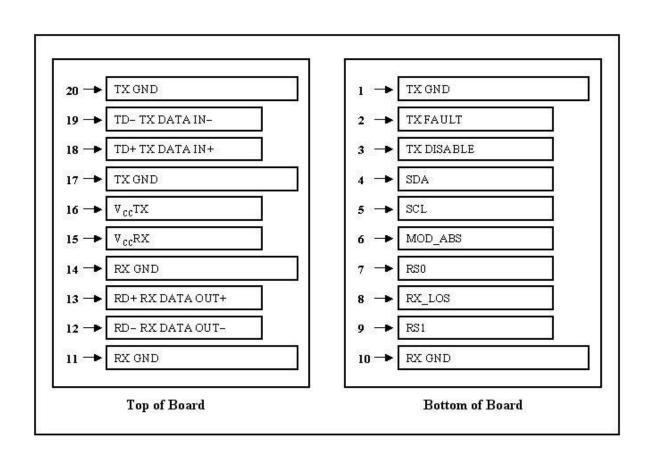
- Datum and Basic Dimension Established by Customer
- Rads and Vias are Chassis Ground, 11 Places
- AThrough Holes are Unplated





## **Electrical Pad Layout**







## **Pin Assignment**

PIN#	Symbol	Description	Remarks
1	$V_{EET}$	Transmitter ground (common with receiver ground)	Circuit ground is isolated from chassis ground
2	T <sub>FAULT</sub>	Transmitter Fault.	Trom onacolo ground
3	T <sub>DIS</sub>	Transmitter Disable. Laser output disable on high or open	Disabled: T <sub>DIS</sub> >2V or open Enabled: T <sub>DIS</sub> <0.8V
4	SDA	Data line for serial ID	Should Be pulled up with
5	SCL	Clock line for serial ID	4.7k – 10k ohm on host
6	MOD_ABS	Module Absent. Grounded within the module	board to a voltage between 2V and 3.6V
7	RS0	No connection required	
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation	LOS is open collector output
9	RS1	No connection required	
10	V <sub>EER</sub>	Receiver ground (common with transmitter ground)	Circuit ground is isolated
11	$V_{EER}$	Receiver ground (common with transmitter ground)	from chassis ground
12	RD-	Receiver Inverted DATA out. AC coupled	
13	RD+	Receiver Non-inverted DATA out. AC coupled	
14	$V_{EER}$	Receiver ground (common with transmitter ground)	Circuit ground is isolated from chassis ground
15	$V_{CCR}$	Receiver power supply	
16	V <sub>CCT</sub>	Transmitter power supply	
17	V <sub>EET</sub>	Transmitter ground (common with receiver ground)	Circuit ground is connected to chassis ground
18	TD+	Transmitter Non-Inverted DATA in. AC coupled	
19	TD-	Transmitter Inverted DATA in. AC coupled	
20	V <sub>EET</sub>	Transmitter ground (common with receiver ground)	Circuit ground is connected to chassis ground

### References

- 1. IEEE standard 802.3ae. IEEE Standard Department, 2005.
- Enhanced 8.5 and 10 Gigabit Small Form Factor Pluggable Module "SFP+" SFF-8431
   Digital Diagnostics Monitoring Interface for Optical Transceivers SFF-8472.